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emulation sleep clocks "device under test" locks

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C Nemecek - US Patent 6,922,821, 2005 - [freepatentsonline.com](#)

... of the FPGA implementation of an **emulator** for the ... or when system encounters a breakpoint (eg, **sleep**, stall, internal ... However, using a **clock**, which is two times ...

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... to the base station, the interface having a **clock** signal me ... microcontroller can be placed in a **sleep** mode so ... use with a combined In-Circuit **Emulation** system and ...

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... If the microcontroller is not in a **sleep** mode, the ... An In-Circuit **Emulation** system consistent with certain trailer perspective. having a microcontroller **clock**. ...

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